DITEL: PRODUCTS: DIGITAL STARS: **7359XY04**

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DESCRIPTION

Model 735 panel indicators are slave displays with four separate BCD parallel input lines per digit. The transfer to display of data contained in the data bus is controlled by a single line (LATCH/STROBE) common to the four digits that can operate with positive or negative logic.

They also provide an input to activate the sign and the decimal point.

Input logic levels are 24V or 5V TTL. Pull-up or pull-down resistors are provided depending on the indicator's logic whether it is positive or negative. Power and signal connections are made by means of a 25-pin AMPEDGE connector located at the rear of the unit.

SELECTION GUIDE

7359	X	Y	0	4
INPUT				
5V TTL logic (-)	0			
5V TTL logic (+)	6			
24V logic (-)	7			
24V logic (+)	8			
POWER SUPPLY				
115V 50/60Hz		1		
230V 50/60Hz		2		
12V DC ISOLATED		4		
24V 50/60Hz		7		
24V DC ISOLATED		8		
SILKSCREENED UNIT				

ORDERING EXAMPLE

7359 8204 E00: BCD parallel indicator S700 Power: 230V AC (50/60Hz) Input: 24Vdc (+) No units Format: 72x36mm 4 digits

SPECIFICATIONS

INPUT SIGNAL

- Data: format 4 bits/digit, code 8.4.2.1.
- Input type: TTL/5V CMOS compatible

Level	TTL/5V	24Vdc
Positive logic	logic 1 > 3Vdc logic 0 < 2Vdc	logic 1 > +10Vdc logic 0 < +5Vdc
Negative logic	logic 1 < 2Vdc logic 0 > 3Vdc	logic 1 < +5Vdc logic 0 > +10Vdc

LATCH/STROBE AND DECIMAL POINT

- Positive logic: Transfers > 1Vdc
- Negative logic: Transfers < 0.5Vdc

POWER

- Supply voltages

AC (50/60Hz): 24, 115, 230V AC

DC (isolated): 12, 24V DC

- Maximum isolation: 1000V DC or 1500V ACpp
- Consumption: 4W nominal

DISPLAY

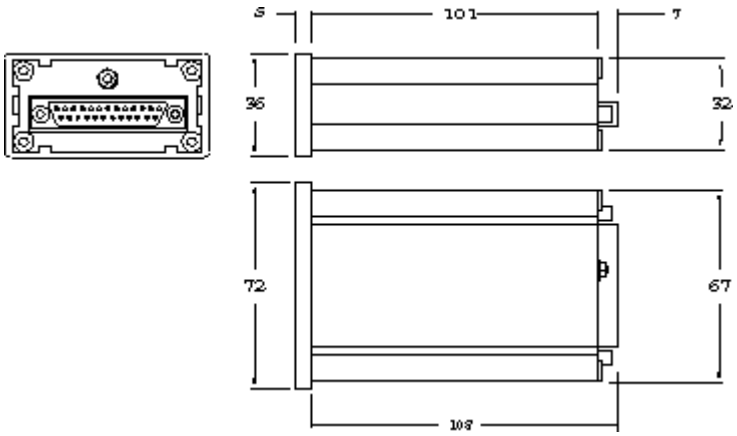
- Type: red LED (0.4") 10 mm. high
- Decimal point: 3, selectable at connector
- Polarity: negative sign
- Data memory: latch
- Decoding: from 0 to 9

(other values cause the display blanked)

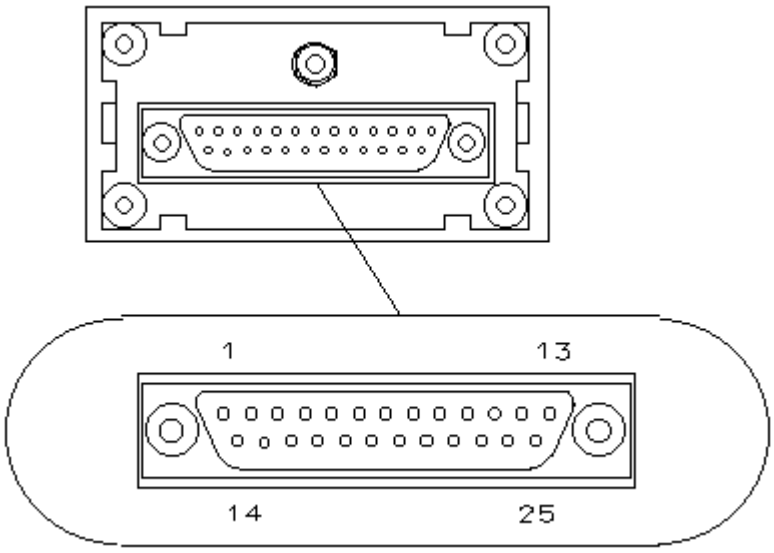
ENVIROMENTAL

- Operating temperature: 0°C to 50°C
- Storage temperature: -25°C to +85°C
- Relative humidity: max. 95% (non condensing)
- Weight: 300g
- Dimensions: 72x36x110mm. (s/DIN 43700)
- Case material: 94 V-0 UL-rated polycarbonate

DIMENSIONS (mm)



SIGNAL AND POWER CONNECTION

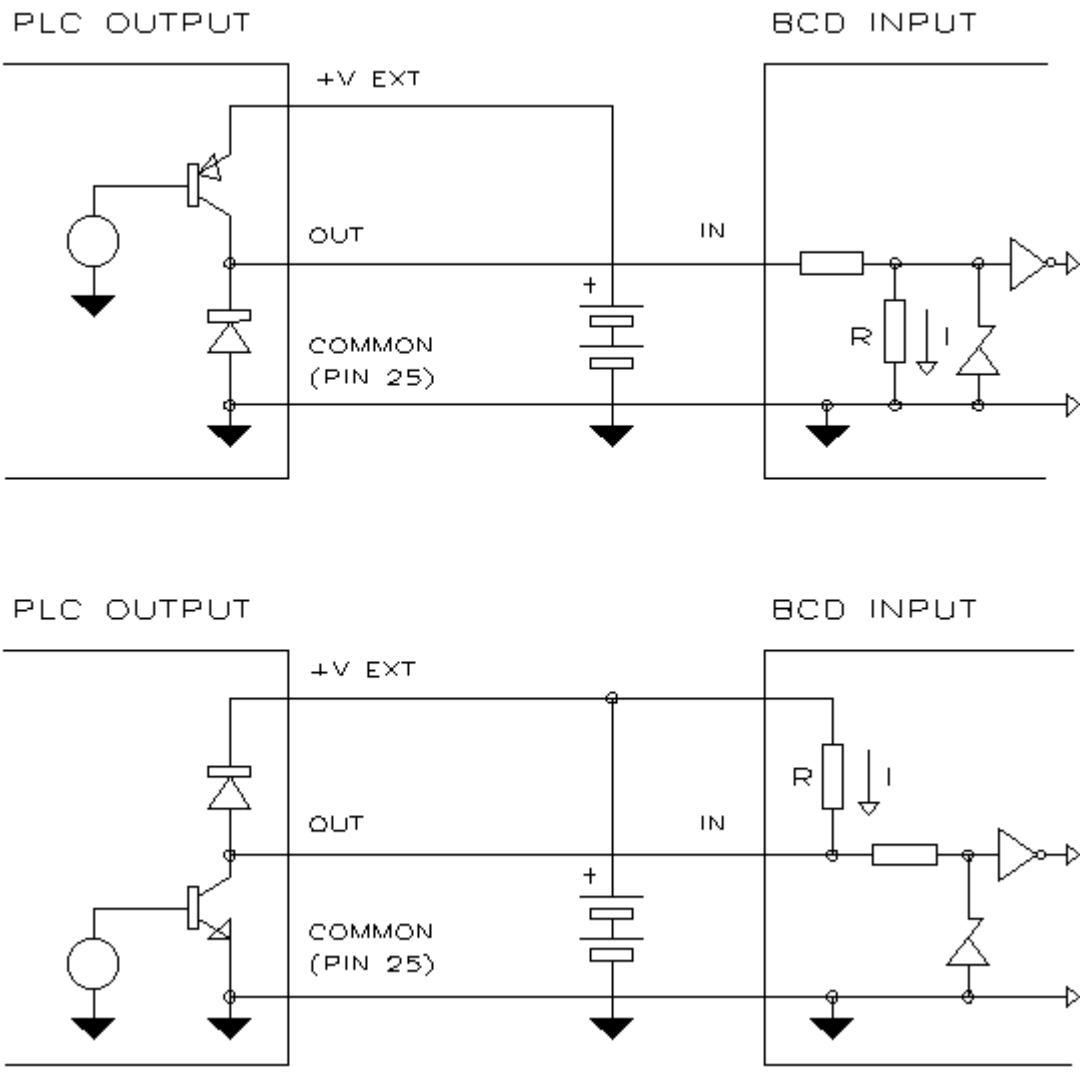


PIN 1 - 1(D0) PIN 14- 1(D2)
PIN 2 - 2(D0) PIN 15- 2(D2)
PIN 3 - 4(D0) PIN 16- 4(D2)
PIN 4 - 8(D0) PIN 17- 8(D2)
PIN 5 - Sign PIN 18- DP (D2)
PIN 6 - 1(D1) PIN 19- 1(D3)
PIN 7 - 2(D1) PIN 20- 2(D3)
PIN 8 - 4(D1) PIN 21- 4(D3)
PIN 9 - 8(D1) PIN 22- 8(D3)
PIN 10- DP (D1) PIN 23- DP (D3)
PIN 11- AC LO / (-) DC PIN 24- Latch
PIN 12- (+)24V DC (pull-up) PIN 25- Common
PIN 13- AC HI / (+) DC

In case of negative logic, pin 12 must be connected to supply 24V to pull-up resistors.

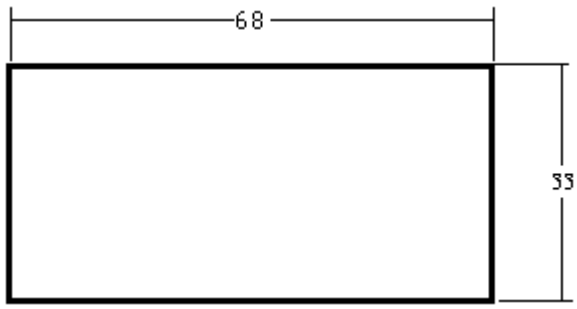
WIRING SCHEMATICS

POSITIVE LOGIC



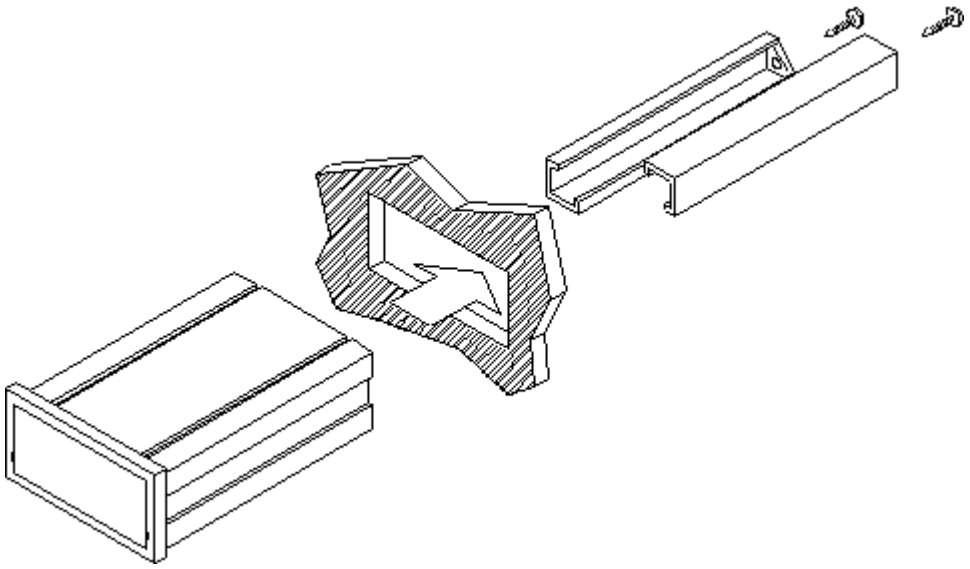
NEGATIVE LOGIC

MOUNTING



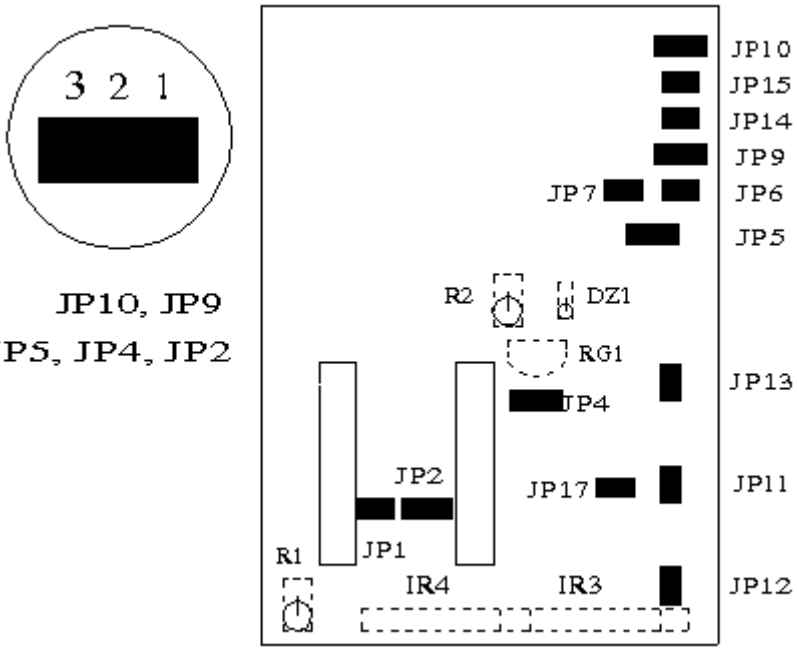
Panel cutout

Min. thickness: 0.8mm
Max. thickness: 10mm

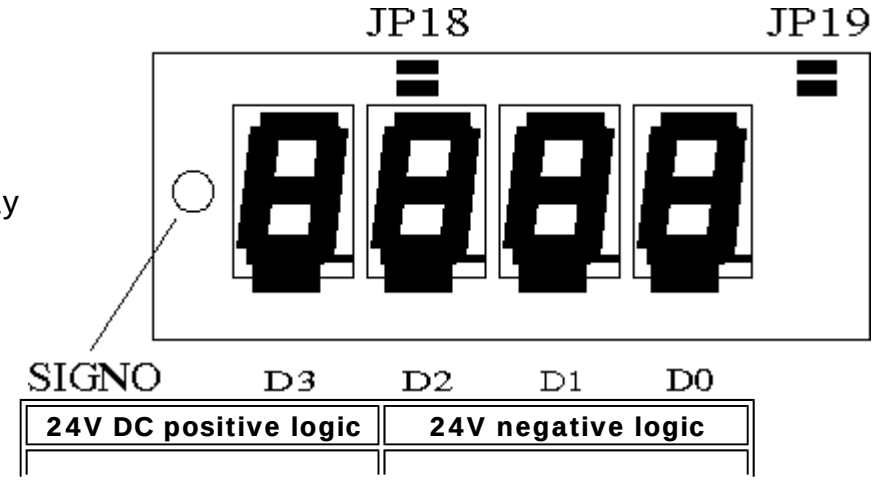


INPUT CONFIGURATION

Main Circuit



Display



Components on board: R2, DZ1, R1, IR3 and IR4 **Eliminate components:** RG1 **Solder jumpers:** DIGITS LATCH JP10 (2-3) JP4 (2-3) JP9 (2-3) JP5 (2-3) JP14, JP1 JP6	**Componenets on board:** R2 and DZ1 **Eliminate components:** RG1, R1, IR3 and IR4 **Solder jumpers:** DIGITS LATCH JP10 (1-2) JP4 (1-2) JP9 (1-2) JP5 (1-2) JP14 JP7

5V TTL positive logic	**5V TTL negative logic**
Components on board: R2, DZ1, R1, IR3 and IR4 **Eliminate components:** RG1 **Solder jumpers:** DIGITS LATCH JP10 (2-3) JP4 (2-3) JP9 (2-3) JP5 (2-3) JP14, JP1 JP6	**Componenets on boatd:** R2 and DZ1 **Eliminate components:** RG1, R1, IR3 and IR4 **Solder jumpers:** DIGITS LATCH JP10 (1-2) JP4 (1-2) JP9 (1-2) JP5 (1-2) JP14 JP7

Sign and decimal point	
Positive logic	**Negative logic**
DECIMAL POINT SIGN JP11, JP12, JP13 JP18	DECIMAL POINT SIGN JP17 JP19

Warranty:
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